

FIG. 1

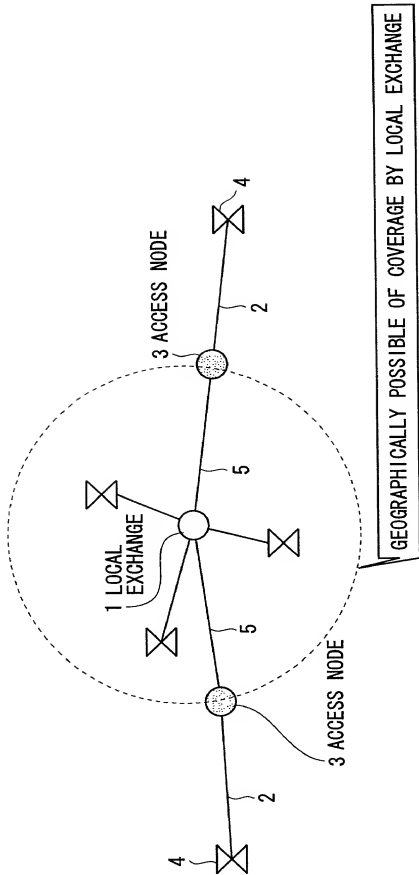


FIG. 2

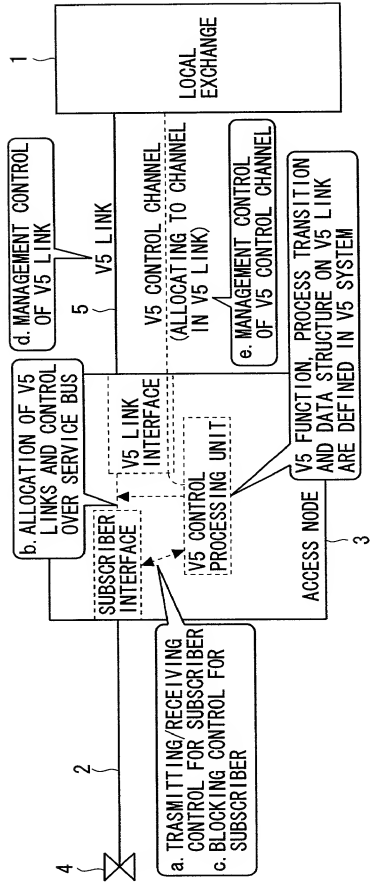


FIG. 3

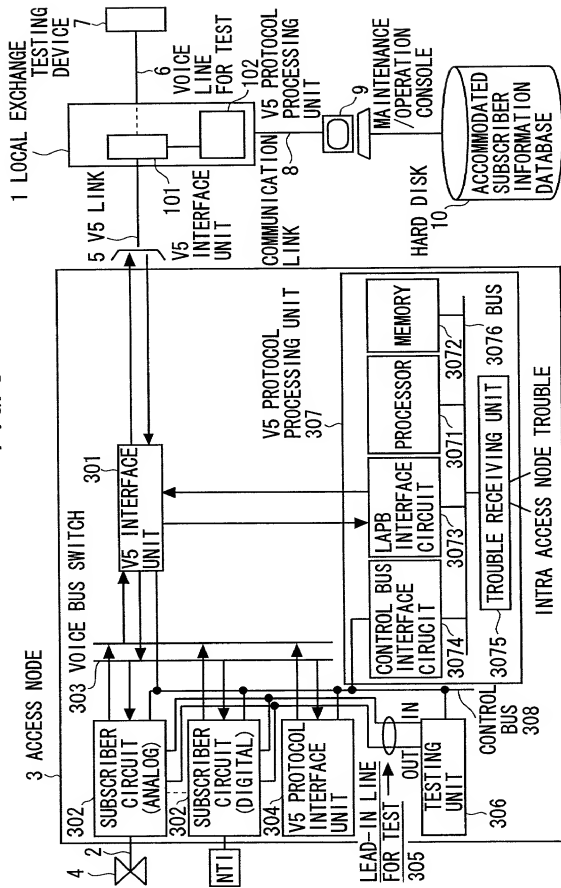
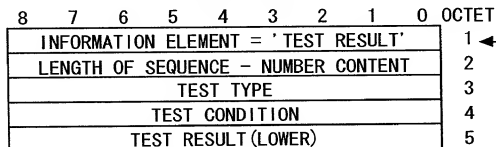
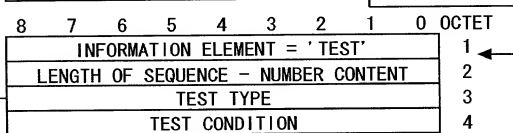
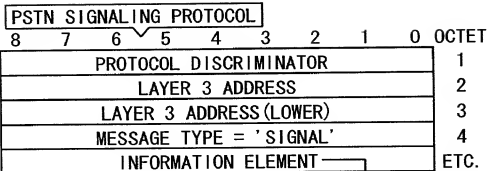


FIG. 4



TEST TYPE	MEANING
0	INTER AB AC VOLTAGE
1	INTER AG AC VOLTAGE
2	INTER BG AC VOLTAGE
3	INTER AB DC VOLTAGE
4	INTER AG DC VOLTAGE
5	INTER BG DC VOLTAGE
6	INTER AB RESISTANCE
7	INTER AG RESISTANCE
8	INTER BG RESISTANCE
9	INTER AB CAPACITY
10	INTER AG CAPACITY
11	INTER BG CAPACITY
12	SUBSCRIBER'S LINE TEST
13-31	UNDEFINED

FIG. 6

PORT CONTROL PROTOCOL									OCTET
8	7	6	5	4	3	2	1	0	
PROTOCOL DISCRIMINATOR									1
LAYER 3 ADDRESS									2
LAYER 3 ADDRESS (LOWER)									3
MESSAGE TYPE = 'PORT CONTROL'									4
INFORMATION ELEMENT									ETC.

8	7	6	5	4	3	2	1	0	OCTET
INFORMATION ELEMENT = 'TEST'									1
LENGTH OF SEQUENCE - NUMBER CONTENT									2
TEST TYPE									3
TEST CONDITION									4

8	7	6	5	4	3	2	1	0	OCTET
INFORMATION ELEMENT = 'TEST RESULT'									1
LENGTH OF SEQUENCE - NUMBER CONTENT									2
TEST TYPE									3
TEST RESULT									4
TEST RESULT (LOWER)									5

TEST TYPE	MEANING
0	INTER AB AC VOLTAGE
1	INTER AG AC VOLTAGE
2	INTER BG AC VOLTAGE
3	INTER AB DC VOLTAGE
4	INTER AG DC VOLTAGE
5	INTER BG DC VOLTAGE
6	INTER AB RESISTANCE
7	INTER AG RESISTANCE
8	INTER BG RESISTANCE
9	INTER AB CAPACITY
10	INTER AG CAPACITY
11	INTER BG CAPACITY
12	AN LOOPBACK
13	NT1 LOOPBACK
12-31	UNDEFINED

FIG. 8

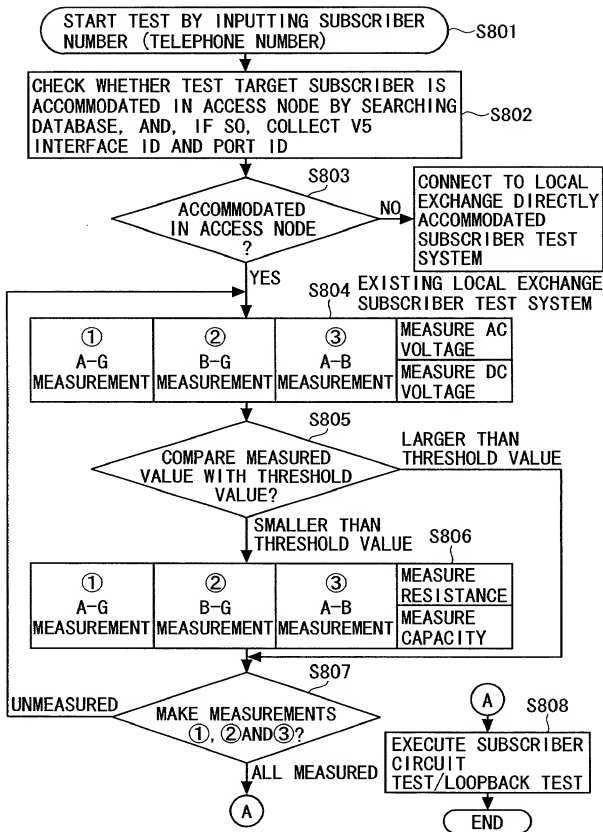


FIG. 9

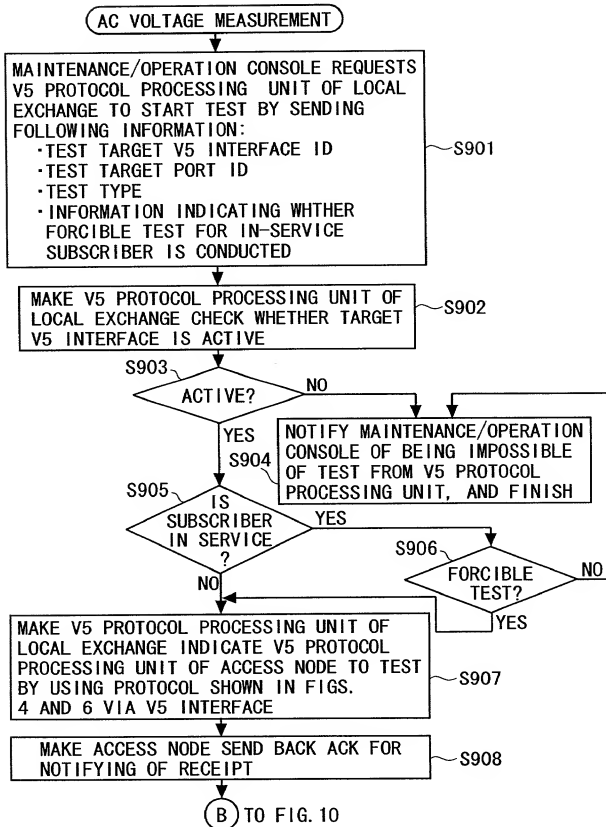


FIG. 10

(B) FROM FIG. 9

